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August 10, 2004

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Certificate of Correction AUG 19 2004

Re: CERTIFICATE OF CORRECTION
U. S. Letters Patent No. 6,764,891 *B2*
Issued: July 20, 2004
For: PHYSICALLY DEFINED VARACTOR IN A
CMOS PROCESS
Inventor: Altmann
Our File No. 42390.P13739

Dear Sir:

Enclosed is the Certificate of Correction (two copies) for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Edwin H. Taylor
Reg. No. 25,129

EHT/jsq
Enclosures

C 9 C
AUG 20 2004

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,764,891 *B2*

DATED : July 20, 2004

INVENTOR(S) : Altmann

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 4, at line 29, delete “P (+well)” and insert --P (+) well--.

MAILING ADDRESS OF SENDER
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN
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Los Angeles, CA 90025-1026

PATENT NO. 6,764,891 *B2*

Certificate of Correction (PTO Form 1050)-Amended

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